MULTICS SYSTEM-PROGRAMMERS ' MANUAL

## SECTION BW.O PAGE 1

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## Identification

Systems on the DEC 338 Display Computer J. M. Grochow

## Introduction

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The Digital Equipment Corporation 338 Buffered Display Computer (located on the 5th floor at Project MAC) is a highly versatile, general purpose computer with a programmable display processor sharing the core memory. The Project MAC installation also has the following auxiliary devices: programmable real-time clock, 2 DEC tape units, a 32k fixed-head disc, four 103A Dataphone interfaces (low-speed) through the telelphone data switch, and a direct telephone line 201B Dataphone (2400 bps) to the GE-645.

The 338 is intended to be used in conjunction with Multics for a variety of purposes. Systems for some of these have been developed and are described in succeeding sections. New sections will be added as design and development of new 338 systems warrant.